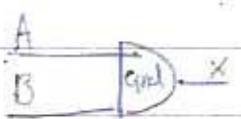


Diagrams for Computing '07

Logic circuits

Find
truth
table
for
Brackets



A	B	X
0	0	0
0	1	0
1	0	0
1	1	1

$$X = AB$$



A	B	X
0	0	0
0	1	1
1	0	1
1	1	1

$$X = A + B$$

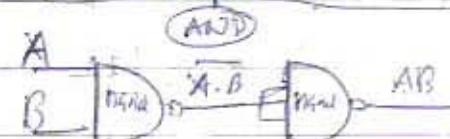
NOR
equivalent

(AND)

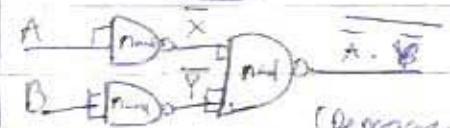
$$X = \overline{A} \cdot \overline{B}$$

$$Y = \overline{A} + \overline{B}$$

equivalent
NAND complete



(OR)



(Complement)

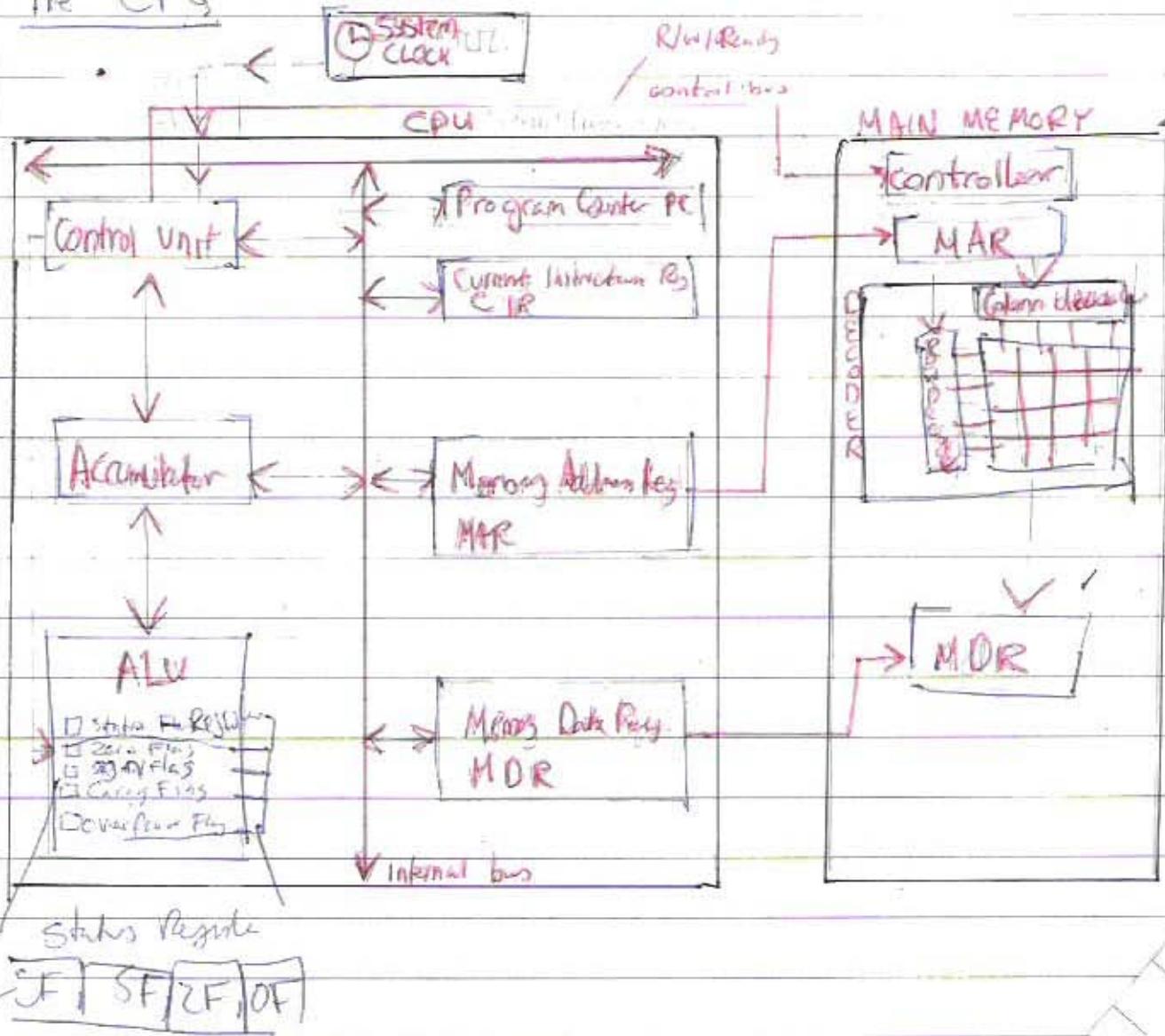
The NOR is equal

but AND type = OR type

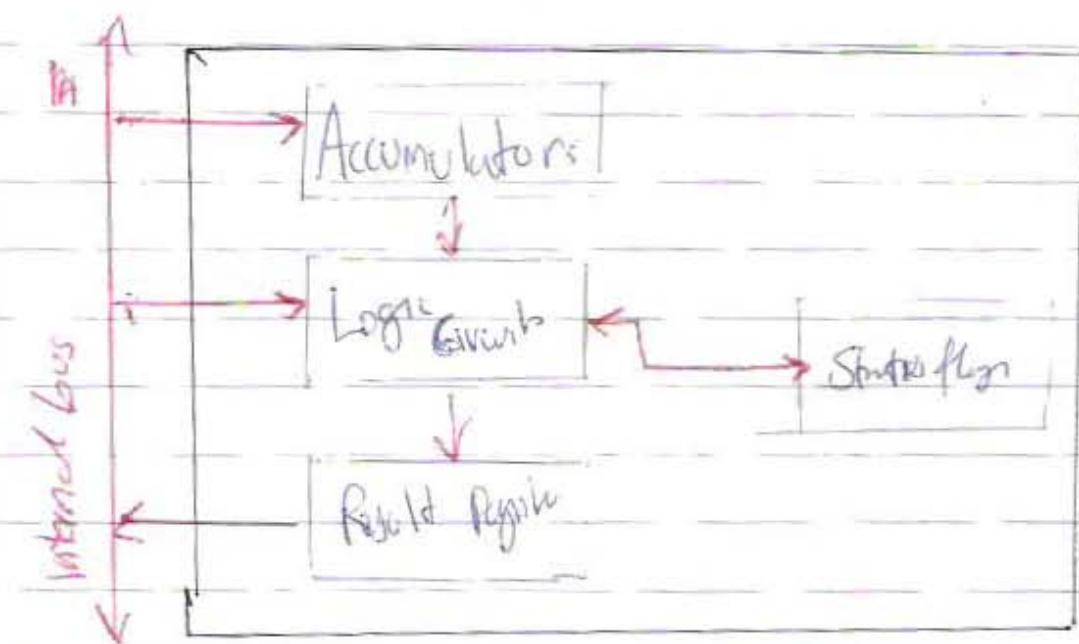
(OR)

$$X = Y$$

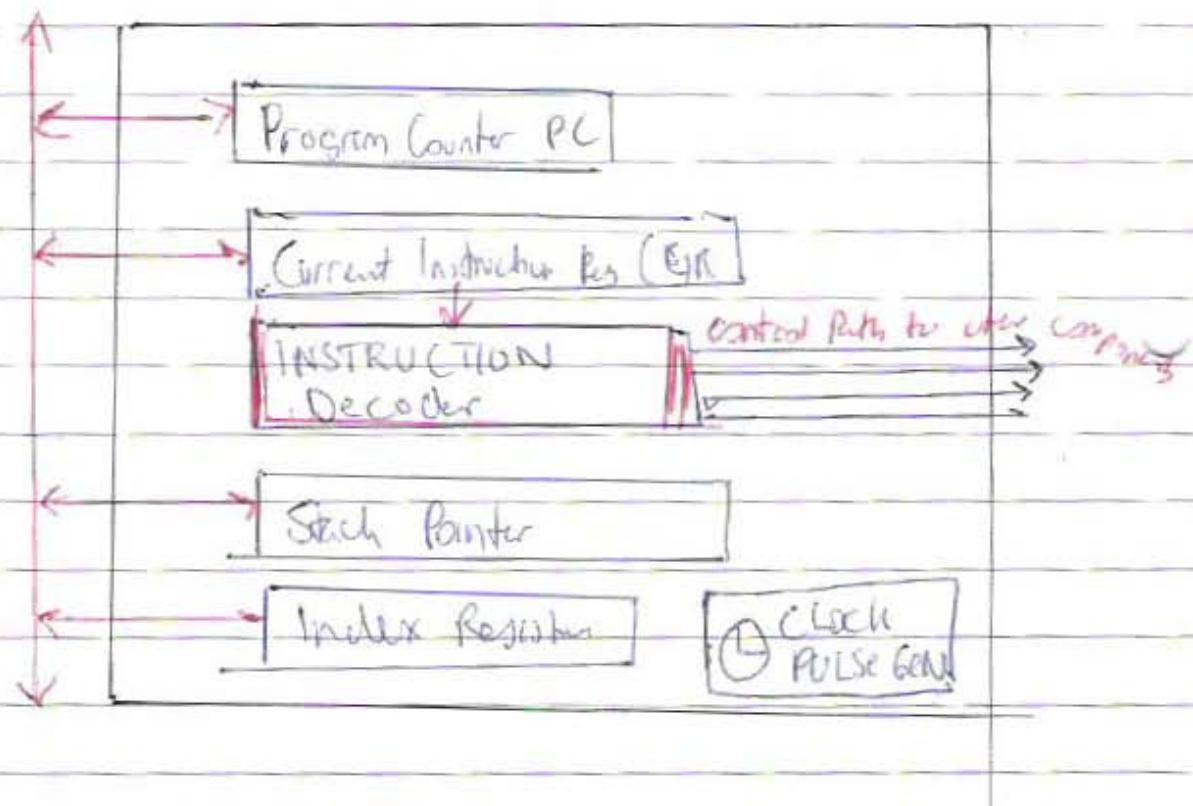
The CPU



ALU



CU

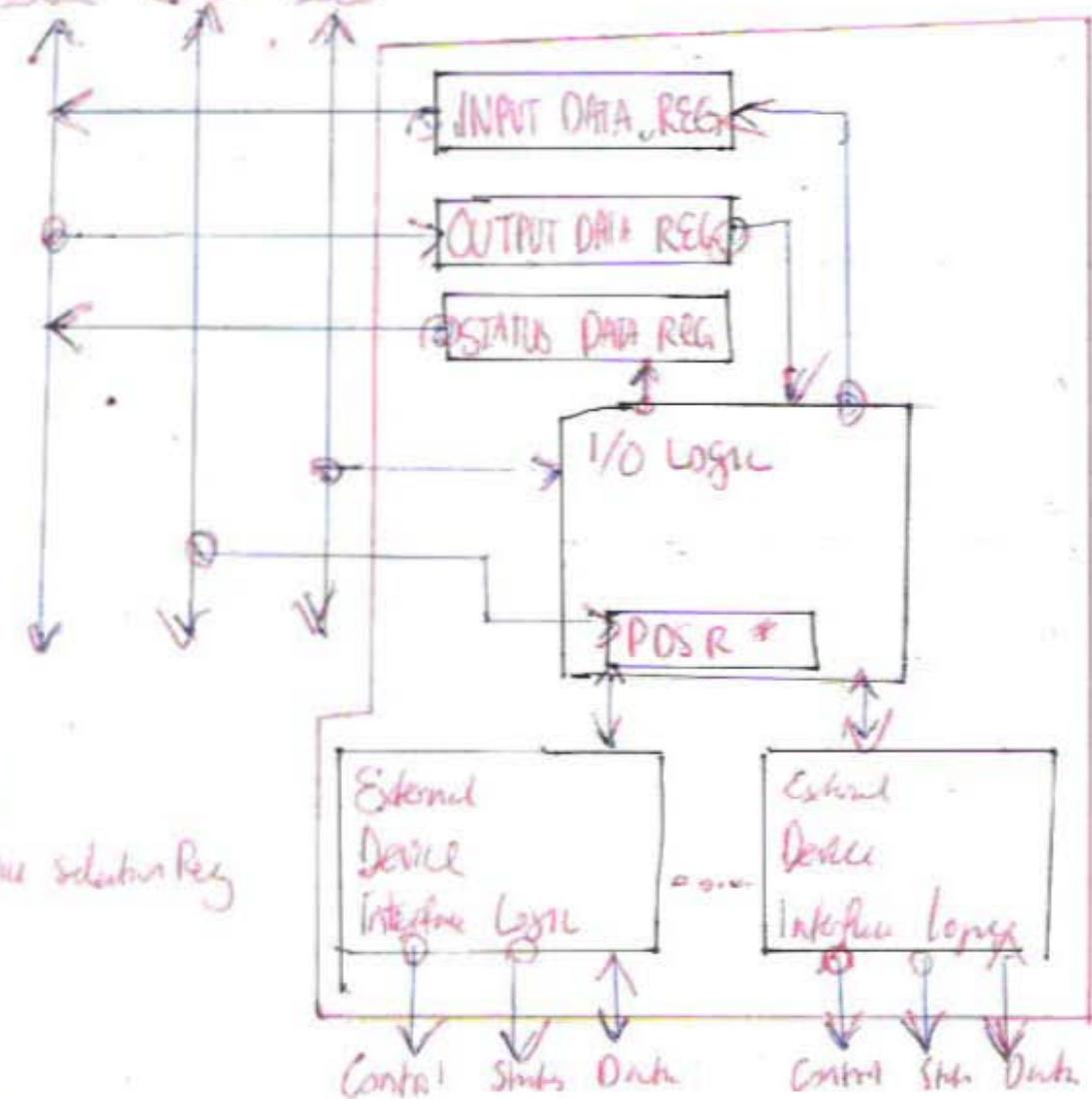


I/O Subsystem

2 Major Func.

1. Provides standard way of how CPU communicates with any external device
2. Shields CPU from hardware complexity

1 bus ABUS (bus)



*PDSR
Peripheral Device Selection Reg

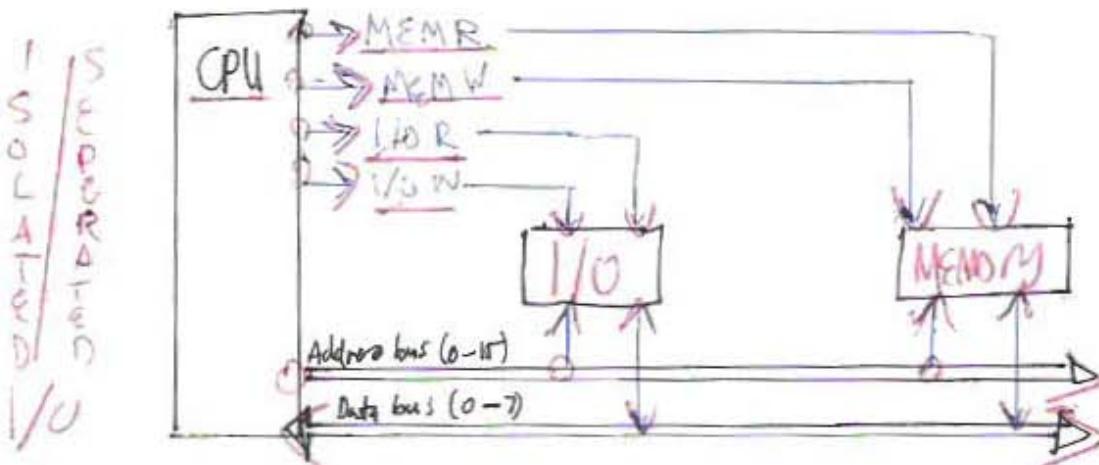
- o The various external devices don't connect directly to System bus
 1. Since there are a link & different range of devices, it won't be impractical to manually tie selecting logic within CPU, it's control a row of devices.
 2. Data transfer rate of external devices is much slow than the CPU or Memory.
 3. external devices use different data formats & word lengths than the computer they are attached to.

2 techniques to identify I/O devices on bus

Isolated I/O

• keeps I/O devices separate from memory addresses.

- Method
- CPU must R/W to particular device
 - it places ~~its own~~ Device address (say Printer - Device no 3) on address bus, then control lines tell I/O device
 - whether to place data on the bus or to read data
 - if I/O device shares bus lines with memory; control signals must tell whether an address on the bus is for I/O device or memory. (see below)



Memory-Mapped I/O: Used ~~to~~ ^{location} to assign addresses of I/O devices on memory. (Must not conflict with add. given to mem [mem])
 I/O devices are read & written into by using the same Control line used for memory. This requires making a map of memory showing which locations or elements to memory & which to I/O devices.

0	ROM
405	
408	
8191	RAM
9472	
9473	Keyboard
9471	I/O
9172	Printer

- Memory mapped I/Os have no I/O instructions in their I. set.
- A ~~lot~~ large number of instructions can be used allowing more efficient programming

• Observe → Valuable memory ^{address} space is used up.

(Next I/O operations)

Memory Map